

Reliability

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CS1, May 23rd, 2007

Many hardware failures

- Storage section
 - replaced all disk controllers
 - some disks broken
- Input section
 - □ 3rd node broken ...
- Software not fault tolerant
 - Future: suspend/restart observation



SAS/MAC integration

SAS/MAC: automatic schedule/start/stop of observations

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- ongoing development
 - InputSection, DelayCompensation work
 - Storage should work
 - BG/L processing to be done



- each pipeline processes 2–4 subbands
- #subbands limited by BG/L I/O bandwidth
 - increase *#pipelines*
 - increase bandwidth per pipeline (next slide)

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the ZOID communication protocol

CPU

CPU

CPL

CPU||CPU

CPUICPL

CPUCPL

CPUIICPU

compute nodes

CPUCPU

CPU

replacement for IBM software faster more flexible en alle run application on I/O node developed with ANL ethernet status: CPU stable, but needs work I/O node 12x3 subbands @160 MHz ok Hee 8x4 subbands ok 12x4 subbands not (?) Hardware for development: use 2nd CPU one processing pipeline