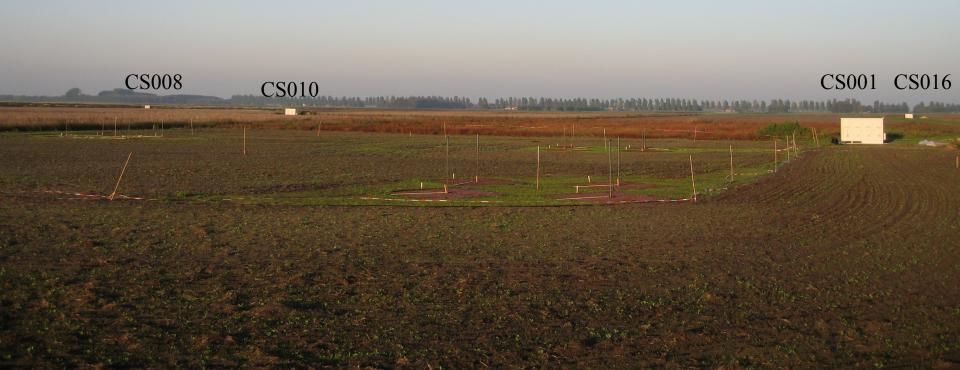
"CS1" Status 27 February 2008

André W. Gunst



News from the field

- WAN data flowing and switches reconfigured
- Currently there is a loop in the network
- Power down of third rack in CS010



TBB Status

External triggers can be used

- Busy with long term integration trigger algorithm
- Bigger FPGA will be selected

	XC4VLX25	XC4VSX35	Ratio
Logic (k)	24	34	1.4
Multipliers	48	192	4
Memory (kbits)	1296	3456	2.7
Cost (\$)	94	122	1.3

