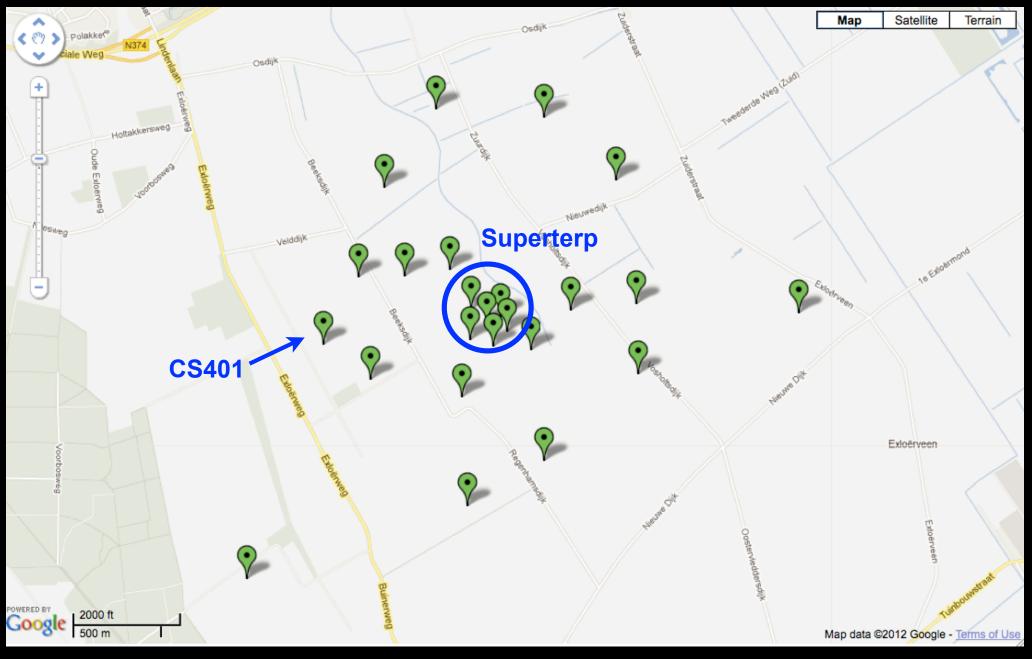
(Full Core) Single Clock Tests

Jason Hessels (ASTRON / UvA) +LOFAR Pulsar Working Group





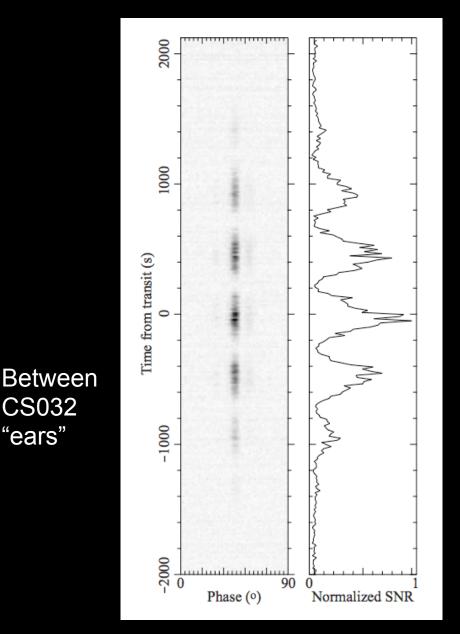
Full-Core Single Clock







Full-Core Single Clock



Try a drift-scan of PSR B0329+54 to map beam and check for coherency.

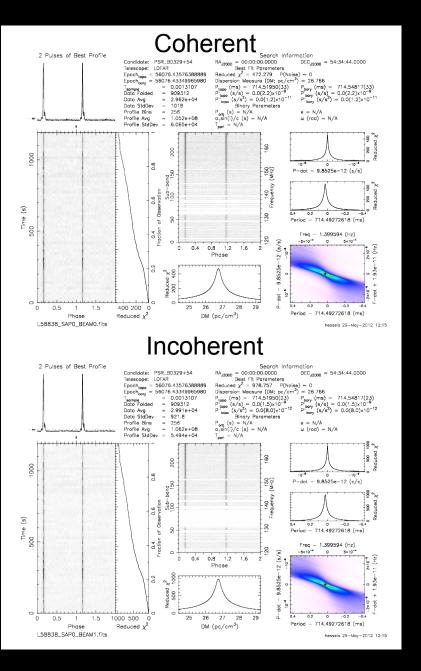
From Stappers, Hessels, Alexov et al. 2011



"ears"



Full-Core Single Clock

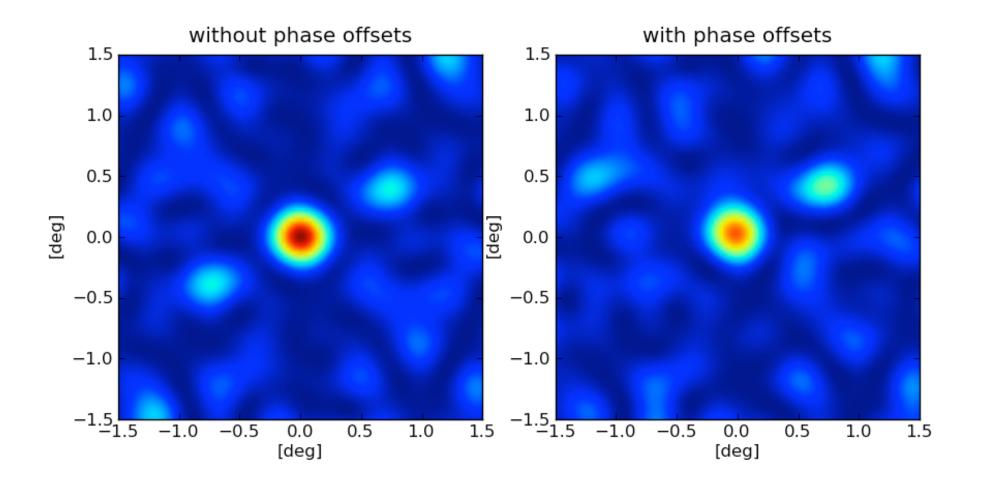


Delays implemented for CS401, but first attempt at drift-scan (yesterday!) does not show the expected results.





Superterp Beam Simulation

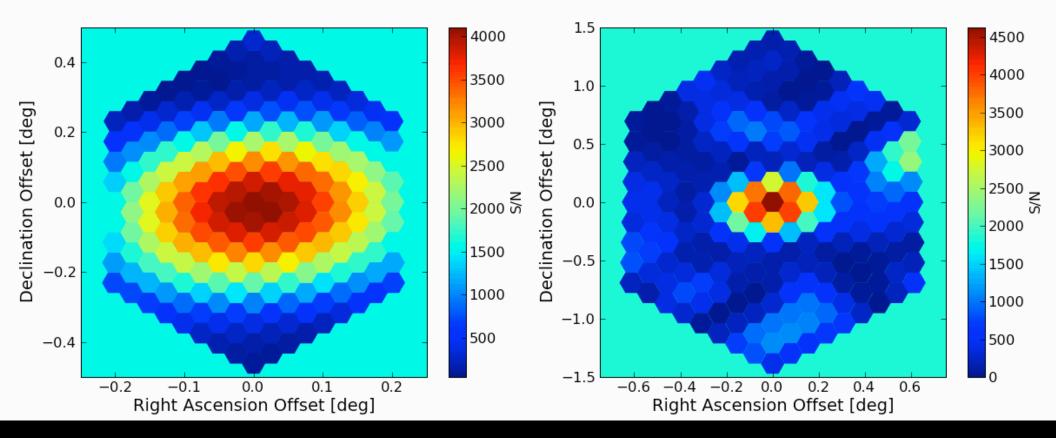


Courtesy Olaf Wucknitz





217+1 Simultaneous Beams



Hessels



LSM - Dwingeloo - May 30th, 2012

AST(RON